

ABSTRACT of the DISCLOSURE

A synchronous burst mode power supply includes a power converter for transforming an AC mains from a relatively low frequency to a higher frequency, and a
5 gate circuit responsive to the AC mains supply for enabling the power converter (2) to initiate a burst of output pulses at the higher frequency each time the AC mains supply occurs within a predetermined range. In an alternative embodiment, the power supply has a regulating circuit for regulating output from the power converter that includes a current feedback loop to the gate circuit for pre-regulating control of the power
10 converter in response to load variations.

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